```
External
               .00
               .LSTON
. Page
       This module contains those Cache routines that must be located
        in Bank 1.
 >
       PROCEDURE Load_Cache
;>>>>>>>>>>>>>>>>>>>>>>>>>>>>>
               .LSTOFF
               .FIN
               . DO
                       External
               .LSTON
               . Page
;>
       Procedure: Load_Cache
;>
;>
;>
       This procedure loads the 'cache-table' with the next 7
;>
        sequential logical blocks ( from the current logical block )
        and status' associated with each block ( i.e., if no seek is
;>
        required, if a head change is required, etc. ) and the
       head and sector number of each logical block ( assuming that
        all the blocks are on the same track ). It should be noted that
        the status bytes are kept in a seperate table.
        Inputs: { none }
       Outputs: { none }
       Algorithm:
        BEGIN
        Data_Type := User_Type
        FOR i := 1 TO CacheLength DO
         Cur_Block := LogicalBlock + i
         TempCyl, TempHead, TempSector,
                       SrchStat, SrchPtr := ChvrtLogical( Cur_Block )
         CacheStatus[ i - 1 ].Seek := CalcMagDir( TempCyl )
         IF ( TempHead <> Head )
          THEN CacheStatus[ i - 1 ].HeadChange := True
          ELSE CacheStatus( i - 1 1.HeadChange := False
         CacheArray( i - 1 1.LogicalBlock := Cur_Block
         CacheArray[ i - 1 ].Head := TempHead
         CacheArray( i - 1 ].Sector := TempSector
        Cache_Index := 0
;>
       END
.LSTOFF
               .FIN
               . DO
                       Internal
               .LSTON
               . Page
               .FIN
```

```
Ld
                         !r4,#CacheLength
                Ld
                         !r5,#0 ;clear seekneeded, headchange booleans
                Ld
                         Data_Tupe, #User_Tupe
                Ld
                         !r8,#.HIBYTE. CachStat
                Ld
                         !r9,#.LOWBYTE. CachStat
                         !rA, #.HIBYTE. CacheArray
                Ld
                Ld
                         !rB,#.LOWBYTE. CacheArray
                Srp
                         #Wrk_Sus2
                                          ;init logical block
                 Call
                         Load_Logical
                 Srp
                         #Wrk_Sys
                 Call
                         Ld_C_Srch
                 Push
                         Flags ; save result of spare table search
                 Push
                         !r0
                 Ld
                         Wrk_Sys2+$09,!rC ;store physical block
                 Ld
                         Wrk_Sys2+$OA,!rD ;store physical block
                         Wrk_Sys2+$0B,!rE ;store physical block
                 Ld
                 Call
                         Cache_Cnvrt
                  Pop
                         !r0
                 Pop
                         Flags
                Cir
                         !r1 ;SeekNeeded,HdChg := False
                 Jr
                         Nz,Ld_Cache_In
                         Ld_C_Enter
                 Jp
Ld_Cache_Lp:
                 Srp
                         #Wrk_Sys2
                         !rE,#1
                 Add
                                          ; inc logical block
                         !rD,#0
                Adc
                 Adc
                         !rC,#0
                 Add
                         !rB,#1
                                          ; inc physical block
                Ade
                         !rA,#0
                Adc
                         !r9,#0
                         #Wrk_Sys
                 Srp
                Call
                         Ld_C_Srch
                 J٣
                         Z, Cache_NoSpare
                 Ld
                         !r1,#CachSeek
                 Or
                         !r5,!r1 ;set seekneeded
Ld_Cache_In:
                Call
                         Enter_Cache
                J٢
                         Ld_Cache_More
                         #Wrk_Sys2
Cache_NoSpare:
                 Srp
                Ld
                         !r0,Wrk_Sys+$0C ;nondestructve subtract
                Ld
                         !r1,Wrk_Sys+$OD
                Ld
                         !r2,Wrk_Sys+$0E
                Sub
                         !r2,!rB ; IF ( TPBlock ↔ PBlock )
                Sbc
                         Ir1, IrA
                Sbc
                         !r0, !r9
                Ld.
                         !rF,#0 ;check distance
                Or
                         !rF, !r0
                0r
                         !rF,!r1
                Or
                         !rF,!r2
                 J٣
                         Z,Ld_Cach_NoSeek
                0r
                         !r0,!r1 ;check for spare block
                J٢
                         Nz.Ld_Cach_Seek
                         !r2,#1 ;should be distance 1
                Cp
                 J٣
                         Nz,Ld_Cach_Seek
                Add
                         !rB,#1 ; inc PBlock to account for spare
                         !rA,#0
                Adc
                         !r9,#0
                Adc
```

Inc !r8 ;bump the sector address Ld_Cach_NoSeek: Inc !r8 ;bump the sector address !r8,#NbrSctrs Ср J۳ Ge, Ld_Cach_HdChg Wrk_Sys+\$01 ;CacheSeek, CachHdChg := False Cir Jr Ld_C_Enter Ld_Cach_HdCha: Ld !r8,#0 ;start at sector 0 Сp !r7,#0 ;check for head 0 Nz,Ld_Cach_Seek J٣ Ld !r7,#1 ;otherwise go to head 1 Wrk_Sys+\$01, #CachHdChg Ld J٣ Ld_C_Enter Ld_Cach_Seek: !r8,#0 ;start at head 0 Ld Ld !r7,#0 ;and sector 0 !r6,#1 ;bump the track count by 1 Add Adc !r5,#0 Ld Wrk_Sys+\$01,#CachSeek Ld_C_Enter: Srp #Wrk_Sus 0r !r5,!r1 ;set seekneeded or headchange Call Ld_BlkStat Dec Ld_Cache_More: 1r4 Nz,Ld_Cache_Lp Jp Ld_Cache_End: Cir Cache_Index Jр Bank_Ret Enter_Cache: Tm !r0,#Spare ;check if block is a spare J٣ Z,Ld_Blk_BB Or !r1,#S_Block ;set Spare Block status Ld_BlkStat J٣ Ld_BIk_BB: Or !r1,#B_Block ;otherwise it must be a Bad Block Ld_BlkStat: 0r !r1,!r5 ;merge in global seekneeded or headchange Lde @!!r8,!r1 Ld_Cach_Lgc1: !r0,#Wrk_Sys2+\$OC ;load CacheArray.Logical Ld Ldei 0!!rA.@!rO Ldei @!!rA,@!r0 Ldei @!!rfl,@!rO !!r8 ;set up for the next go 'round Incw !r0,Wrk_Sys2+\$08 ;get the latest sector value Ld Ld !r2,#.HIBYTE. Map_Table ;logically remap the sector Ld !r3, #.LOWBYTE. Map_Table Add !r3,!r0 Adc !r2,#0

Lde

Ld

Swap

!r0.@!!r2

!r1,Wrk_Sys2+\$07 ;get latest head value

!r1 ;merge Head with sector

```
RI
                        lr1
               RI
                       In1
               0r
                       !r1,!r0
               Lde
                       @!!rA,!r1
                incw
                       !!rA ;point to next cache entry
               Ret
Cache_Cnvrt:
               Ld
                        !rC,Wrk_Sys2+$09 ;get latest Physical block
                        !rD, Wrk_Sus2+$OA
               Ld
                        !rE.Wrk_Sys2+$0B
               Ld
                Ld
                        !r2,#.HIBYTE. Get_Cyl_H_S
                Ld
                        !r3,#.LOWBYTE. Get_Cyl_H_S
               Call
                       Bank_Call
                       Wrk_Sys2+$05,!rC ;store latest seek address
               Ld
               Ld
                       Wrk_Sys2+$06, !rD
               Ld
                       Wrk_Sys2+$07, !rE
               Ld
                       Wrk_Sys2+$08, !rF
               Ret
                . PAGE
Fast search of spare table
Ld_C_Srch:
               Ld
                        !rC,Wrk_Sys2+$0C ;get last logical block
                        !rD,Wrk_Sys2+$0D
               Ld
                       !rE,Wrk_Sys2+$0E
               Ld
               Ld
                        !r1,!rD ;check if head ptr is NIL
                        !r0,!rC ;but first form a headptr structure
               Ld
                               ;and index into HeadPtr Array
               Brc
                        !r0
               Brc
                        !r1
               Ref
               Brc
                        !r1
                       !r2,#.HIBYTE. SegPtrArray
               Ld
                        !r3, #.LOWBYTE. SegPtrArray
               Ld
               Add
                        !r3,!r1
                        !r2,#0
               Ade
               Lde
                        !r0,@!!r2
                                       ;get headptr and check for NIL
                        !r0.#Nil
                Tm
                       Z,Ld_C_Long ;do a real search if not NIL
                J٣
                . DO
                       W_10MB
               Ld
                        !r0,!rD ;save for possible rollover
                Add
                        !rE, !rD ;Physical Block := LBlock + LBlock DIV 256
                Ade
                       !rD,#0
                Adc
                        !rC,#0
                .FIN
                Ср
                        !r0,!rD ;check for rollover
                J۳
                       Z,Ld_C_S_End
                        !rE,#1 ;otherwise bump Physical Block
                Add
```

Ref

Adc !rD,#0 Adc !rC,#0

Ld_C_S_End: Ld_C_S_Ret: Xor Ret !r0,!r0 ;return zero status

Ld_C_Long:

Ld Ld

!r2,#.HIBYTE. SrchSpTabl !r3,#.LOWBYTE. SrchSpTabl Bank_Call Ld_C_S_Ret

Call Bank.
Jr Ld_C.

.LSTOFF